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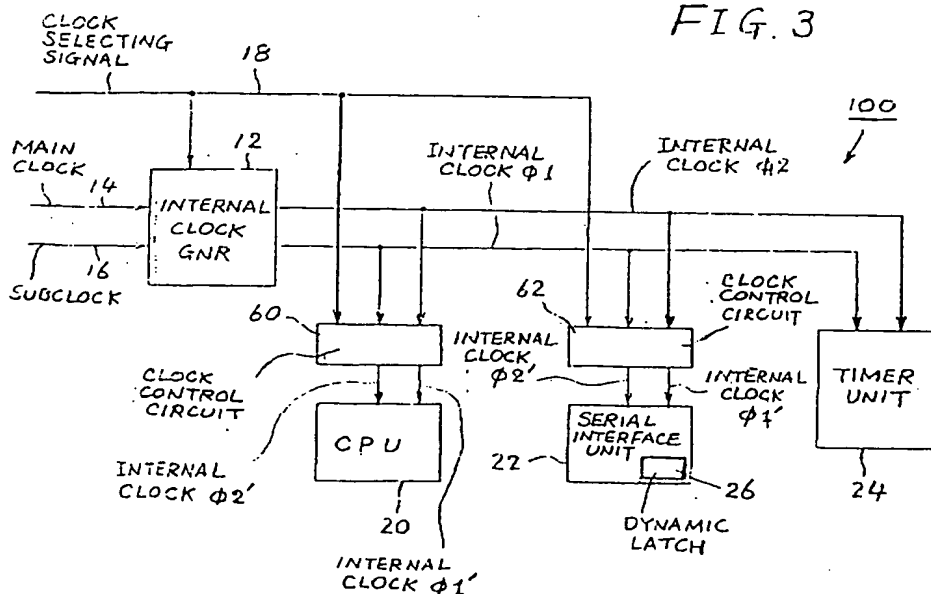
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01.06.94 Bulletin 94/2271 Applicant: NEC CORPORATION
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54 Microcomputer having logic circuit for prohibiting application of subclock to selected internal unit.

57 A microcomputer includes a plurality of units operating in synchronism with a first internal clock or a second internal clock depending on operation modes of the microcomputer. The clock rate of the second internal clock is lower than that of the first

internal clock. The microcomputer is provided with a clock control circuit which precedes at least one unit. The clock control circuit prevents the second internal clock from being applied to the unit and allows the first internal clock to be applied to the unit.



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EUROPEAN SEARCH REPORT

Application Number
— EP 91 11 7748

DOCUMENTS CONSIDERED TO BE RELEVANT

Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int.CLS)
X P,X	JP-A-2 082 306 (...) 22 March 1990 & US-A-5 025 387 (TERRIE L.FRANE) 18 June 1991 * the whole document *	1-5 1-5	G06F1/04 G06F1/32 G06F1/08
X	US-A-3 594 656 (YOSHIHIRO TSUKAMOTO) 20 July 1971 * column 2, line 8 - line 72 * * figure 1 *	1-5	
			TECHNICAL FIELDS SEARCHED (Int.CLS)
			G06F
The present search report has been drawn up for all claims			
Place of search	Date of completion of the search	Examiner	
THE HAGUE	6 April 1994	Ciarelli, N	
CATEGORY OF CITED DOCUMENTS			
X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document		T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons * : member of the same patent family, corresponding document	

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